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GROUP ART UNIT 2823

INFORMATION DISCLOSURE STATEMENT

FORM PTO-1449

(REV 7-80)

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
16	AA	6,001,731	12/99	Su et al.	438	633	
16	AB	5,783,482	07/98	Lee et al.	438	624	
6	AC	5,437,763	08/95	Huang .	216	18	
21	ΑĐ	5,518,962	05/96	Murao	438	624	
14	ΑE	4,983,546	01/91	Hyun et al.	438	800	
Kt	AF	5,567,661	10/96	Nishio et al.	438	631	
X1	AG**	5,169,491	12/92	Doan	156	636	
16	AH**	5,560,802	10/96	Chisholm	156	636.1	
K+	A1**	5,5661,084	08/97	Kuo et al.	438	624	

FOREIGN PATENT DOCUMENTS

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANS	LATION
		NUMBER						
		·			ļ		YES	NO
11	AJ**	0599317A1	06/01/94	Europe	H01L	21/90		
14	AK**	09223740	08/26/97	Japan (Abstract Only)	H01L	21/768		
1/2	AL**	08255791	10/01/96	Japan (Abstract Only)	H01L	21/316		

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

1					
V/	AM	Wolf, Stanley, PhD., Silicon Processing for the VLSI Era, Volume 2-Process Integration,			
166		Lattice Press, Sunset Beach, Volume 2: 334-337, 1990.			
1//	AN	Schaffer, W.J., et al., "BPSG Improves CPMP Performance for Deep Submicron Ics,"			
Kt	1	Semiconductor International, 205-212, 1996.			
VL	AO	Armstrong, W.E., et al., "A Scanning Electron Microscope Investigation of Glass Flow in			
12	1	MOS Integrated Circuit Fabrication," Journal of the Electrochemical Society, Volume 121			
	1	No. 2, 307-310, 1974.			
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0		Research and Development, Volume 8, No. 4, 376-384, 1964.			
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* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).